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PART B - FEE(S) TRANSMITTAL

Slater & Matsil, L.L.P. 9727329218 2/3

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25962 7590 07/26/2004

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10/25/2004 AKELECH2 00000161 501065 10762973

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Kristin R. Hayes (Depositor's name)
K. Hayes (Signature)
October 25, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/762,973	01/22/2004	Rolf Weis	01P14627 US01	6352

TITLE OF INVENTION: METHOD OF FORMING AN ALIGNMENT MARK STRUCTURE USING STANDARD PROCESS STEPS FOR FORMING VERTICAL GATE TRANSISTORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$4300 \$1370	\$300	\$4700 \$1670	10/26/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
VESPERMAN, WILLIAM C	2813	438-401000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list
(1) the names of up to 3 registered patent attorneys or agents OR, alternatively, Slater & Matsil, L.L.P.
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)
PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE Infineon Technologies AG
(B) RESIDENCE: (CITY AND STATE OR COUNTRY) Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) Ira S. Matsil, 35,272 (Date) October 25, 2004

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FACSIMILE COVER SHEET

To Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (703) 746-4000	Transmission Date: October 25, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Weis Docket No.: 01P14627 US01
Serial No: 10/762,973 Art Unit: 2813
Date Filed: January 22, 2004
Title: Method of Forming An Alignment Mark Structure Using Standard Process Steps
for Forming Vertical Gate Transistors

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 746-4000 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Part B- Fee Transmittal (1 original and 1 copy)

Respectfully submitted,

Kristin Hayes
Legal Assistant

Confirmation Respectfully Requested

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